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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/245,269	02/05/1999	JACK A MANDELMAN	99P7451US	4716

7590 12/07/2001

SIEMENS CORPORATION
INTELLECTUAL PROPERTY DEPARTMENT
186 WOOD AVENUE SOUTH
ISELIN, NJ 08830

EXAMINER

NGUYEN, DILINH P

ART UNIT	PAPER NUMBER
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2814

DATE MAILED: 12/07/2001

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/245,269

Applicant(s)

MANDELMAN ET AL.

Examiner

DiLinh Nguyen

Art Unit

2814

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 05 February 1999.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) 3-20 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-2 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) 3.
- 4) ☐ Interview Summary (PTO-413) Paper No(s). _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other:

DETAILED ACTION

Election/Restrictions

Applicant's election with traverse of Group I, in Paper No. 6 is acknowledged. The traversal is on the grounds that Groups I and II are neither independent nor distinct and claims 1-17 should be examined together in the same application. Groups III and IV are neither independent nor distinct and claims 18-20 should be examined together in the same application. This is not found persuasive because Groups I-IV are patentably independent and distinct.

Regarding Groups I and II, unpatentability of Group I invention would not necessarily imply unpatentability of the Group II invention and otherwise. Groups I and II are related as combination and subcombination. Groups I and II have acquired a separate status in the art as shown by their different classification. Group I, claims 1-2, drawn to a semiconductor device, particularly to the electrical isolation for semiconductor structures in a semiconductor body. Group II, claims 3-17, drawn to a dynamic random access memory.

Regarding Groups III and IV, unpatentability of Group III invention would not necessarily imply unpatentability of the Group IV invention and otherwise. Groups III and IV have acquired a separate status in the art as shown by their different classification. Group III, claims 18-19, drawn to a method of providing electrical isolation for semiconductor structures contained in a semiconductor body. Group IV, claim 20, drawn to a method for making memory cells in a semiconductor body of one conductivity type.

The requirement is still deemed proper and is therefore made FINAL.

Claim Rejections - 35 USC § 112

1. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

2. Claims 1-2 are rejected under 35 U.S.C. 112, first paragraph, as containing subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention.

In claim 1, the phrase "...an isolation trench....encircling an area of the semiconductor body..." is not understood. The trench is rectangular and does not encircle anything. Appropriate correction is required.

In claim 1, the phrase "...encircling an area..." is not understood.

In claim 1, lines 12-13, the phrase "...a lower portion that is in electrical contact with the semiconductor body..." is not understood. How does it contact the semiconductor body?

3. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

4. Claim 1 recites the limitation "upper and lower portions" in line 5. There is insufficient antecedent basis for this limitation in the claim.

Claim Rejections - 35 USC § 102

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

6. Claims 1-2 are rejected under 35 U.S.C. 102(e) as being anticipated by Wada (U.S. Pat. 6274919).

Wada discloses a semiconductor device (Fig. 2A-2D, column 4, lines 47 et seq.) comprising:

a semiconductor body defining an isolation trench 12 having sidewalls and an upper and a lower portions;

the semiconductor body contains a semiconductor structures, wherein the semiconductor structures electrically isolated from each other;

the lower portion of the isolation trench being at least partly filled with an electrically conductive material 24 (column 4, lines 63-65) that has sidewall portions thereof which are at least partly separated from the sidewalls of the lower portion of the trench by a first electrical insulator 23 (column 4, lines 59-60); and

the upper portion of the isolation trench being filled with a second electrical insulator 9 (column 4, lines 16-17).

- Regarding claim 2, Wada discloses the electrically conductive material is doped polysilicon and first and second electrical insulators are both silicon dioxide.


Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DiLinh Nguyen whose telephone number is (703) 305-6983. The examiner can normally be reached on 8:00AM - 6:00PM (M-F).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, OLIK CHAUDHURI can be reached on (703) 308-2794. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 308-7722 for regular communications and (703) 308-7724 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

DLN
November 29, 2001


Douglas Wille
Patent Examiner